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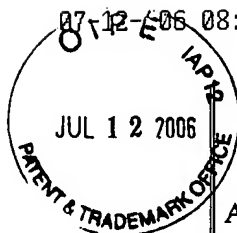
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From:	Greg J. Michelson	Date Sent:	
Subject:	Applicant: Agrawal et al. Serial No. 10/619,645	Time Sent:	
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Om P. Agrawal; Bai Nguyen; Kuang Chi; Brad Sharpe-Geisler; Giap Tran
Assignee: Lattice Semiconductor Corporation
Title: Scalable Serializer-Deserializer Architecture And Programmable Interface
Serial No.: 10/619,645 Filing Date: 07/14/2003
Examiner: Jason Crawford Group Art Unit: 2819
Docket No.: M-15135 US Confirmation No.: 9559

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July 12, 2006

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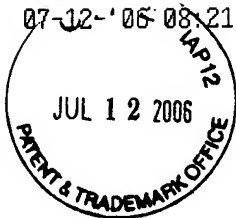

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-1-

Serial No. 10/619,645

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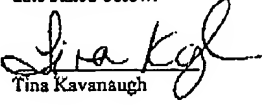
Alexandria, VA 22313-1450

Re: Applicant(s): Om P. Agrawal, Bai Nguyen, Kuang Chi, Brad Sharpe-
Geisler, and Giap Tran
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Serial No.: 10/619,645 Filed: 7/14/2003
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Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

1. Facsimile Cover Sheet (1 page)
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Respectfully submitted,



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